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ISSUE CLASSIFICATION

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U.S. REISSUE Patent Application

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
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Clock Phase Detecting circuit and clock regenerating circuit each arranged in receiving unit of multiplex radio equipment.

PTO-2040
12/89

SURRENDER OF ORIGINAL PATENT _____ **(Exr. Initials)** **ORIGINAL PATENT NUMBER** _____

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

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		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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FINAL SPRE REVIEW					ISSUE BATCH NUMBER	
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